

Notice of References Cited	Application/Control No. 09/881,254	Applicant(s)/Patent Under Reexamination HSHIEH ET AL.	
	Examiner Johannes P Mondt	Art Unit 2826	Page 1 of 1

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	A	US-4,116,720	09-1978	Vinson	148/1.5
	B	US-4,250,519	02-1981	Mogi et al.	357/41
	C	US-5,025,293	06-1991	Seki	357/23.4
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	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

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	U	Wolf, S., and Tauber, S.N., "Silicon Processing for the VLSI Era", Volume 1 - Process Technology, 2000, Published by Lattice Press, Sunset Beach, CA (USA), Second Edition, p. 206
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*A copy of this reference is not being furnished with this Office action (See MPEP § 707.05(a).)
 Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.